

**JARO Components, Inc.**

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Tel : 561-241-6700 Fax : 561-241-3328

**SPECIFICATION**

**Product Name : General Purpose Multilayer Ceramic Chip**

**Capacitors**

**Description : Size 0402, C0G(NPO), 27pF, ±5%, 50V**

<b>SPEC. No. : CC-056-001-01</b>
<b>DATE : 2018/10/25</b>

<b>DRAWN BY</b>	<b>CHECEKED BY</b>	<b>APPROVED BY</b>
<i>Jane Hsiao</i>	<i>Yvens Chou</i>	<i>Joseph Ling</i>

## 1. INTRODUCTION

JARO Multilayer Ceramic Chip Capacitors supplied in bulk or tape & reel package are ideally suitable for thick-film hybrid circuits and automatic surface mounting on any printed circuit boards.

The nickel-barrier terminations are consisted of a nickel barrier layer over the silver metallization and then finished by electroplated solder layer to ensure the terminations have good solderability. The nickel barrier layer in terminations prevents the dissolution of termination when extended immersion in molten solder at elevated solder temperature.

## 2. FEATURES

- a. A wide selection of sizes is available (0201 to 2225).
- b. High capacitance in given case size.
- c. Capacitor with lead-free termination (pure Tin).
- d. RoHS & HALOGEN compliant.

## 3. APPLICATIONS

- a. For general digital circuit.
- b. For power supply bypass capacitors.
- c. For consumer electronics.
- d. For telecommunication.
- e. DC to DC converter.

## 4. HOW TO ORDER

CC	0805	CG	101	J	T	50	ER
SERIES	CASE SIZE	DIELECTRIC	CAPACITANCE	TOLERANCE	TERMINATION	VOLTAGE	PACKAGING
CC = Standard	0201(0603)	CG = NPO	First two	C = ± 0.25 pf	T = Tin Terminations	16	ER = Tape & Reel
	0402(1005)	XR = X7R	digits significant.	D = ± 0.5 pf	(RoHS Compliant)	25	PR = Paper Tape /
	0603(1608)	ZU = Z5U	Third digit indicates	F = ± 1%		50	Tape & Reel
	0805(2012)	YV = Y5V	number of zeros.	G = ± 2%		100	
	1206(3216)			J = ± 5%		250	
	1210(3225)			K = ± 10%		500	
	1812(4532)			M = ± 20%		102	
	1825(4563)			Z = +80%, -20%		152	
	2220(5750)			P = +100, -0%		202	
	2225(5763)						

## 5. EXTERNAL DIMENSIONS

Size Inch (mm)	L (mm)	W (mm)	Code / T (mm)	M <sub>B</sub> (mm)	
0201(0603)	0.60±0.03 0.60±0.05 (Cap.≥0.68μF)	0.30±0.03 0.30±0.05 (Cap.≥0.68μF)	See No.4 Reference Table 8	0.15±0.05	
0402(1005)	1.00±0.10 1.00±0.20 <sup>#1</sup>	0.50±0.10 0.50±0.20 <sup>#1</sup>		0.25 +0.05/-0.10	
0603(1608)	1.60±0.15	0.80±0.15		0.40±0.15	
0805(2012)	2.00±0.20	1.25±0.20		0.50±0.20	
1206(3216)	3.20±0.20 3.20 +0.30/-0.10 <sup>#2</sup>	1.60±0.20 1.60 +0.30/-0.10 <sup>#2</sup>		0.60±0.20	
1210(3225)	3.20±0.30	2.50±0.30		0.75±0.35	
1812(4532)	4.50±0.40	3.20±0.30		0.75±0.35	
1825(4563)	4.50±0.40	6.30±0.40		0.75±0.35	
2220(5750)	5.70±0.40	5.00±0.40		0.85±0.35	
2225(5763)	5.70±0.40	6.30±0.40		0.85±0.35	

Fig. 5.1 The outline of MLCC

<sup>#1</sup> For 0402 size K thickness products. <sup>#2</sup> For 1206 size P thickness products.

## 6. GENERAL ELECTRICAL DATA

Dielectric	C0G	X7R	X5R	Y5V
Size	0201, 0402, 0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, 2225	0201, 0402, 0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, 2225	0201, 0402, 0603	0201, 0402, 0603, 0805, 1206, 1210, 1812
Rated voltage (WVDC)	10V, 16V, 25V, 50V	6.3V, 10V, 16V, 25V, 50V	4V, 6.3V, 10V, 16V, 25V, 50V	6.3V, 10V, 16V, 25V, 50V
Capacitance range*	0R1 to 100nF	100pF to 820nF	100pF to 820nF	10nF to 680nF
Capacitance tolerance**	J(±5%), K(±10%)	J(±5%), K(±10%), M(±20%)		Z(-20/+80%)
Tan δ*	Cap.<30pF : Q≥400+20C Cap.≥30pF : Q≥1000	Note 1		
Operating temperature	-55 to +125°C		-55 to +85°C	-25 to +85°C
Capacitance characteristic	±30ppm/°C	±15%		+30/-80%
Termination	Cu or Ag/Ni/Sn or Au (lead-free termination)			

\* Measured at the condition of 30~70% related humidity.

C0G : Apply 1.0±0.2Vrms, 1.0MHz±10% for Cap.≤1000pF and 1.0±0.2Vrms, 1.0KHz±10% for Cap.>1000pF, 25°C at ambient temperature.

X7R/X5R : Apply 1.0±0.2Vrms, 1.0KHz±10%, at 25°C ambient temperature.

Y5V : Apply 1.0±0.2Vrms, 1.0KHz±10%, at 20°C ambient temperature.

\*\* Preconditioning for Class II MLCC : Perform a heat treatment at 150±10°C for 1 hour, then leave in ambient condition for 24±2 hours before measurement.

Note 1 : X7R/X5R

Rated	D.F.≤	Exception of D.F.≤	
50V	≤3.5%	≤3.5%	0201(50V), 0603≥0.047μF, 0805≥0.1μF, 1206≥0.47μF
	≤5%	≤5%	0201≥0.01μF,
		≤10%	0402≥0.1μF, 0603>0.1μF
25V	≤3.5%	≤5%	0201≥0.01μF
		≤7%	0603≥0.33μF
		≤10%	0201≥0.1μF, 0402≥0.10μF, 0603≥0.47μF
		≤12.5%	0402≥0.47μF
16V	≤3.5%	≤5%	0201≥0.01μF, 0402≥0.033μF, 0603≥0.15μF, 0805≥0.68μF,
		≤10%	0201≥0.1μF(0201/X7R≥0.022μF), 0402≥0.22μF, 0603≥0.68μF,
10V	≤5%	≤10%	0201≥0.012μF, 0402≥0.33μF(0402/X7R≥0.22μF), 0603≥0.33μF,
		≤15%	0201≥0.1μF
6.3V	≤10%	≤15%	0201≥0.1μF
4V	≤15%	---	---

Y5V

Rated	D.F.≤	Exception of D.F.≤	
50V	≤5%	≤7%	0603≥0.1μF, 0805≥0.47μF
35V	≤7%	---	---
25V	≤5%	≤7%	0402≥0.047μF, 0603≥0.1μF, 0805≥0.33μF,
		≤9%	0402≥0.068μF, 0603≥0.47μF
16V (C<1.0μF)	≤7%	≤9%	0402≥0.068μF, 0603≥0.68μF
		≤12.5%	0402≥0.22μF
10V	≤12.5%	≤20%	0402≥0.47μF
6.3V	≤20%	---	---

## 7. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Condition	Requirements																																										
1.	Visual and Dimensions	---	* No remarkable defect. * Dimensions to confirm to individual specification sheet.																																										
2.	Capacitance		* Shall not exceed the limits given in the detailed spec.																																										
3.	Q/D.F. (Dissipation Factor)	* Class I : (C0G) Cap.≤1000pF, 1.0±0.2Vrms, 1MHz±10%. Cap.>1000pF, 1.0±0.2Vrms, 1KHz±10%. * Class II : (X7R, X5R, Y5V) Cap.≤10μF, 1.0±0.2Vrms, 1KHz±10%**. Cap.>10μF, 0.5±0.2Vrms, 120Hz±20%. ** Test condition : 0.5±0.2Vrms, 1KHz±10%. X5R : 01R5≥103, 0201≥224(6.3V,10V,16V) <sup>#1</sup> ,	* C0G : Cap.≥30pF, Q≥1000; Cap.<30pF, Q≥400+20C. *X7R/X5R : <table border="1"> <thead> <tr> <th>Rated</th> <th>D.F.≤</th> <th colspan="2">Exception of D.F.≤</th> </tr> </thead> <tbody> <tr> <td rowspan="3">50V</td> <td rowspan="3">≤2.5%</td> <td>≤3.5%</td> <td>0201(50V), 0603≥0.047μF, 0805≥0.1μF, 1206≥0.47μF</td> </tr> <tr> <td>≤5%</td> <td>0201≥0.01uF</td> </tr> <tr> <td>≤10%</td> <td>0402≥0.1μF, 0603&gt;0.1μF</td> </tr> <tr> <td rowspan="4">25V</td> <td rowspan="4">≤3.5%</td> <td>≤5%</td> <td>0201≥0.01μF</td> </tr> <tr> <td>≤7%</td> <td>0603≥0.33μF</td> </tr> <tr> <td>≤10%</td> <td>0201≥0.1μF, 0402≥0.10μF, 0603≥0.47μF,</td> </tr> <tr> <td>≤12.5%</td> <td>0402≥0.47μF</td> </tr> <tr> <td rowspan="2">16V</td> <td rowspan="2">≤3.5%</td> <td>≤5%</td> <td>0201≥0.01μF, 0402≥0.033μF, 0603≥0.15μF, 0805≥0.68μF</td> </tr> <tr> <td>≤10%</td> <td>0201≥0.1uF(0201/X7R≥0.022μF), 0402≥0.22uF, 0603≥0.68μF</td> </tr> <tr> <td rowspan="2">10V</td> <td rowspan="2">≤5%</td> <td>≤10%</td> <td>0201≥0.012μF, 0402≥0.33μF(0402/X7R≥0.22μF), 0603≥0.33μF</td> </tr> <tr> <td>≤15%</td> <td>0201≥0.1μF</td> </tr> <tr> <td>6.3V</td> <td>≤10%</td> <td>≤15%</td> <td>0201≥0.1μF</td> </tr> <tr> <td>4V</td> <td>≤15%</td> <td>---</td> <td>---</td> </tr> </tbody> </table>	Rated	D.F.≤	Exception of D.F.≤		50V	≤2.5%	≤3.5%	0201(50V), 0603≥0.047μF, 0805≥0.1μF, 1206≥0.47μF	≤5%	0201≥0.01uF	≤10%	0402≥0.1μF, 0603>0.1μF	25V	≤3.5%	≤5%	0201≥0.01μF	≤7%	0603≥0.33μF	≤10%	0201≥0.1μF, 0402≥0.10μF, 0603≥0.47μF,	≤12.5%	0402≥0.47μF	16V	≤3.5%	≤5%	0201≥0.01μF, 0402≥0.033μF, 0603≥0.15μF, 0805≥0.68μF	≤10%	0201≥0.1uF(0201/X7R≥0.022μF), 0402≥0.22uF, 0603≥0.68μF	10V	≤5%	≤10%	0201≥0.012μF, 0402≥0.33μF(0402/X7R≥0.22μF), 0603≥0.33μF	≤15%	0201≥0.1μF	6.3V	≤10%	≤15%	0201≥0.1μF	4V	≤15%	---	---
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5.	Insulation Resistance	<p>* To apply rated voltage for Max. 120sec.</p>	<p>* <math>\geq 10G\Omega</math> or <math>RxC \geq 500\Omega \cdot F</math>, whichever is smaller.                      * Except :</p> <table border="1"> <thead> <tr> <th>Rated voltage (X7R/X5R/Y5V)</th> <th>I.R.</th> </tr> </thead> <tbody> <tr> <td>50V : 0402 &gt; 0.01μF</td> <td rowspan="4"><math>\geq 10G\Omega</math> or <math>RxC \geq 100\Omega \cdot F</math>, whichever is smaller</td> </tr> <tr> <td>16V : 0201 <math>\geq 0.1\mu F</math>, 0402 <math>\geq 0.22\mu F</math></td> </tr> <tr> <td>10V : 0201 <math>\geq 47nF</math>, 0402 <math>\geq 0.47\mu F</math>, 0603 <math>\geq 0.47\mu F</math>, 6.3V; 4V</td> </tr> <tr> <td>Rated voltage (X7R/X5R/Y5V)</td> </tr> <tr> <td>50V : 0402 <math>\geq 0.1\mu F</math></td> <td rowspan="5"><math>RxC \geq 50\Omega \cdot F</math></td> </tr> <tr> <td>25V : 0201 <math>\geq 0.1\mu F</math></td> </tr> <tr> <td>16V : 0603 <math>\geq 10\mu F</math>, 0402 <math>\geq 1\mu F</math>, 0201 <math>\geq 0.22\mu F</math></td> </tr> <tr> <td>10V : 0201 &gt; 0.1μF</td> </tr> <tr> <td>6.3V : 0201 <math>\geq 0.1\mu F</math></td> </tr> </tbody> </table>	Rated voltage (X7R/X5R/Y5V)	I.R.	50V : 0402 > 0.01μF	$\geq 10G\Omega$ or $RxC \geq 100\Omega \cdot F$ , whichever is smaller	16V : 0201 $\geq 0.1\mu F$ , 0402 $\geq 0.22\mu F$	10V : 0201 $\geq 47nF$ , 0402 $\geq 0.47\mu F$ , 0603 $\geq 0.47\mu F$ , 6.3V; 4V	Rated voltage (X7R/X5R/Y5V)	50V : 0402 $\geq 0.1\mu F$	$RxC \geq 50\Omega \cdot F$	25V : 0201 $\geq 0.1\mu F$	16V : 0603 $\geq 10\mu F$ , 0402 $\geq 1\mu F$ , 0201 $\geq 0.22\mu F$	10V : 0201 > 0.1μF	6.3V : 0201 $\geq 0.1\mu F$																		
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## 7. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Condition	Requirements															
6.	<b>Dielectric Strength</b>	* To apply 250% of rated voltage. * Duration : 1 to 5 sec. * Charge and discharge current less than 50mA.	* No evidence of damage or flash over during test.															
7.	<b>Solderability</b>	* Solder temperature : 235±5°C for (0201~1210). * Solder temperature : 245±5°C for (1808~2225). * Dipping time : 2±0.5 sec.	* 75% min. coverage of all metalized area.															
8.	<b>Resistance to Soldering Heat</b>	* Solder temperature : 260±5°C. * Dipping time : 10±1 sec. * Preheating : 120 to 150°C for 1 minute before immerse the capacitor in a eutectic solder. * Before initial measurement (Class II only) : To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) or 48±4 hrs (Class II).	* No remarkable damage. * Cap. change : COG : Within ±2.5% or ±0.25pF, whichever is larger. X7R, X5R : Within ±7.5%. Y5V : Within ±20%. * D.F.(Q)/I.R. : To meet initial requirements. * 25% max. leaching on each edge.															
9.	<b>Temperature Cycle</b>	* Conduct the five cycles according to the temperatures and time. <table border="1" data-bbox="264 1361 772 1512"> <thead> <tr> <th>Step</th> <th>Temp.(°C)</th> <th>Time(min.)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Min. operating temp. +0/-3</td> <td>30±3</td> </tr> <tr> <td>2</td> <td>Room temp.</td> <td>2~3</td> </tr> <tr> <td>3</td> <td>Max. operating temp. +3/-0</td> <td>30±3</td> </tr> <tr> <td>4</td> <td>Room temp.</td> <td>2~3</td> </tr> </tbody> </table> * Before initial measurement (Class II only) : To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) or 48±4 hrs (Class II).	Step	Temp.(°C)	Time(min.)	1	Min. operating temp. +0/-3	30±3	2	Room temp.	2~3	3	Max. operating temp. +3/-0	30±3	4	Room temp.	2~3	* No remarkable damage. * Cap. change : COG : Within ±2.5% or ±0.25pF, whichever is larger. X7R, X5R : Within ±7.5%. Y5V : Within ±20%. * Q for COG : To meet initial requirements. * D.F.(Class II) : ≤150% of initial requirement. * I.R. : To meet initial requirements.
Step	Temp.(°C)	Time(min.)																
1	Min. operating temp. +0/-3	30±3																
2	Room temp.	2~3																
3	Max. operating temp. +3/-0	30±3																
4	Room temp.	2~3																

## 7. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

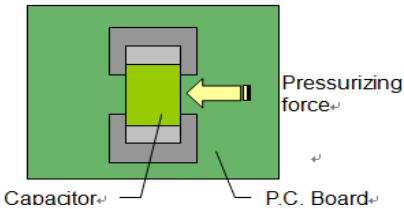
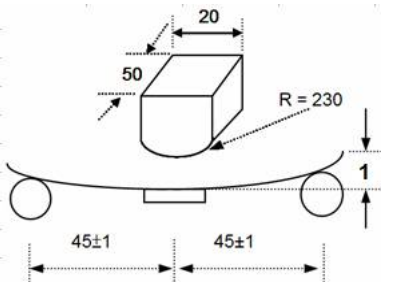
No.	Item	Test Condition	Requirements								
10.	Humidity (Damp Heat) Steady State	<p>* Test temp. : 40±2°C.            * Humidity : 90~95% RH.            * Test time : 500 +24/-0hrs.            * Before initial measurement (Class II only) : To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.            * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) or 48±4 hrs (Class II).</p>	<p>* No remarkable damage.            * Cap. change :            COG : Within ±5.0% or ±0.5pF, whichever is larger.            X7R, X5R : Within ±12.5% for ≥10V**, within ±25% for 6.3V.            **10V : Within ±25% for 0603≥4.7μF, 0402≥1μF, 0201≥0.1μF.            Y5V : Within ±30% for ≥10V, within +30/-40% for 6.3V.            * Q for COG :            Cap.&gt;30pF, Q≥350.            10pF≤Cap.≤30pF, Q≥275+2.5C.            Cap.&lt;10pF, Q≥200+10C.            * D.F.(Class II) : ≤200% of initial requirement.            * I.R. : ≥10V, ≥1GΩ or R×C≥50Ω-F, whichever is smaller.            Class II (X7R, X5R, Y5V)</p> <table border="1"> <thead> <tr> <th>Rated voltage</th> <th>I.R.</th> </tr> </thead> <tbody> <tr> <td>50V : 0402&gt;0.01μF</td> <td rowspan="5">≥1GΩ or R×C≥10Ω-F, whichever is smaller</td> </tr> <tr> <td>25V : 0201≥0.1uF, 0402≥0.22μF</td> </tr> <tr> <td>16V : 0201≥0.1uF, 0402≥0.22μF</td> </tr> <tr> <td>10V : 0201≥47nF, 0402≥0.47μF, 0603≥0.47μF,</td> </tr> <tr> <td>6.3V; 4V; Size≥1812</td> </tr> </tbody> </table>	Rated voltage	I.R.	50V : 0402>0.01μF	≥1GΩ or R×C≥10Ω-F, whichever is smaller	25V : 0201≥0.1uF, 0402≥0.22μF	16V : 0201≥0.1uF, 0402≥0.22μF	10V : 0201≥47nF, 0402≥0.47μF, 0603≥0.47μF,	6.3V; 4V; Size≥1812
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6.3V; 4V; Size≥1812											
11.	Humidity (Damp Heat) Load	<p>* Test temp. : 40±2°C.            * Humidity : 90~95%RH.            * Test time : 500 +24/-0hrs.            * To apply voltage : Rated voltage.            * Before initial measurement (Class II only) : To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.            * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) or 48±4 hrs (Class II).</p>	<p>* No remarkable damage.            * Cap. change :            COG : Within ±7.5% or ±0.75pF, whichever is larger.            X7R, X5R : Within ±12.5% for ≥10V**, within ±25% for 6.3V.            **10V : Within ±25% for 0603≥4.7μF, 0402≥1μF, 0201≥0.1μF.            Y5V : Within ±30% for ≥10V, within +30/-40% for 6.3V.            * Q for COG : Cap.≥30pF, Q≥200; Cap.&lt;30pF, Q≥100+10/3C.            * D.F.(Class II) : ≤200% of initial requirement.            * I.R. : ≥10V, ≥500MΩ or R×C≥25Ω-F, whichever is smaller.            Class II (X7R, X5R, Y5V)</p> <table border="1"> <thead> <tr> <th>Rated voltage</th> <th>I.R.</th> </tr> </thead> <tbody> <tr> <td>50V : 0402&gt;0.01μF</td> <td rowspan="5">≥500MΩ or R×C≥5Ω-F, whichever is smaller</td> </tr> <tr> <td>25V : 0201≥0.1uF, 0402≥0.22μF</td> </tr> <tr> <td>16V : 0201≥0.1uF, 0402≥0.22μF</td> </tr> <tr> <td>10V : 0201≥47nF, 0402≥0.47μF, 0603≥0.47μF,</td> </tr> <tr> <td>6.3V; 4V; Size≥1812</td> </tr> </tbody> </table>	Rated voltage	I.R.	50V : 0402>0.01μF	≥500MΩ or R×C≥5Ω-F, whichever is smaller	25V : 0201≥0.1uF, 0402≥0.22μF	16V : 0201≥0.1uF, 0402≥0.22μF	10V : 0201≥47nF, 0402≥0.47μF, 0603≥0.47μF,	6.3V; 4V; Size≥1812
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## 7. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Condition	Requirements																																															
12.	High Temperature Load (Endurance)	<p>* Test temp. :</p> <p>C0G, X7R : 125±3°C. X5R, Y5V : 85±3°C.</p> <p>* To apply voltage :</p> <p>(1) ≤6.3V : 150% of rated voltage. (2) 10V≤Ur≤50V : 200% of rated voltage. (3) 100% of rated voltage for below range :</p> <table border="1"> <thead> <tr> <th>Size</th> <th>Dielectric</th> <th>Rated</th> <th>Capacitance range</th> </tr> </thead> <tbody> <tr> <td rowspan="2">0201</td> <td rowspan="2">X5R/X7R</td> <td>≤10V</td> <td>C≥0.1μF</td> </tr> <tr> <td>≥16V</td> <td>C&gt;0.1μF</td> </tr> </tbody> </table> <p>(4) 150% of rated voltage for below range :</p> <table border="1"> <thead> <tr> <th>Size</th> <th>Dielectric</th> <th>Rated Voltage</th> <th>Capacitance</th> </tr> </thead> <tbody> <tr> <td rowspan="2">0201</td> <td>X5R/X7R</td> <td>16V/25V</td> <td>C≥0.1μF</td> </tr> <tr> <td>X7R</td> <td>16V</td> <td>C≥0.022μF</td> </tr> <tr> <td rowspan="2">0402</td> <td rowspan="2">X5R/X7R</td> <td>50V</td> <td>C≥0.1μF</td> </tr> <tr> <td>10~25V</td> <td>C≥0.22μF</td> </tr> <tr> <td></td> <td>Y5V</td> <td>16V</td> <td>C≥0.47μF</td> </tr> <tr> <td>0603</td> <td>X7R</td> <td>≥50V</td> <td>C≥0.082μF</td> </tr> <tr> <td>0805</td> <td>X5R/X7R</td> <td>50V</td> <td>C≥0.47μF</td> </tr> </tbody> </table> <p>* Test time : 1000 +24/-0 hrs. * Before initial measurement (Class II only) : To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp. * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) or 48±4 hrs (Class II). ** De-rating conditions :</p>	Size	Dielectric	Rated	Capacitance range	0201	X5R/X7R	≤10V	C≥0.1μF	≥16V	C>0.1μF	Size	Dielectric	Rated Voltage	Capacitance	0201	X5R/X7R	16V/25V	C≥0.1μF	X7R	16V	C≥0.022μF	0402	X5R/X7R	50V	C≥0.1μF	10~25V	C≥0.22μF		Y5V	16V	C≥0.47μF	0603	X7R	≥50V	C≥0.082μF	0805	X5R/X7R	50V	C≥0.47μF	<p>* No remarkable damage. * Cap. change :</p> <p>C0G : Within ±3.0% or ±0.3pF, whichever is larger. X7R, X5R : Within ±12.5% for ≥10V**, within ±25% for ≤6.3V. **10V : Within ±25% for 0603≥4.7μF, 0402≥1μF, 0201≥0.1μF. Y5V : Within ±30% for ≥10V, within +30/-40% for ≤6.3V.</p> <p>* Q for C0G :</p> <p>Cap.&gt;30pF, Q≥350. 10pF≤Cap.≤30pF, Q≥275+2.5C. Cap.&lt;10pF, Q≥200+10C.</p> <p>* D.F.(Class II) : ≤200% of initial requirement. * I.R. : ≥10V, ≥1GΩ or RxC≥50Ω-F, whichever is smaller. Class II (X7R, X5R, Y5V)</p> <table border="1"> <thead> <tr> <th>Rated voltage</th> <th>I.R.</th> </tr> </thead> <tbody> <tr> <td>50V : 0402&gt;0.01μF</td> <td rowspan="5">≥1GΩ or RxC≥10Ω-F, whichever is smaller</td> </tr> <tr> <td>25V : 0201≥0.1uF, 0402≥0.22μF</td> </tr> <tr> <td>16V : 0201≥0.1uF, 0402≥0.22μF</td> </tr> <tr> <td>10V : 0201≥47nF, 0402≥0.47μF, 0603≥0.47μF,</td> </tr> <tr> <td>6.3V; 4V; Size≥1812</td> </tr> </tbody> </table>	Rated voltage	I.R.	50V : 0402>0.01μF	≥1GΩ or RxC≥10Ω-F, whichever is smaller	25V : 0201≥0.1uF, 0402≥0.22μF	16V : 0201≥0.1uF, 0402≥0.22μF	10V : 0201≥47nF, 0402≥0.47μF, 0603≥0.47μF,	6.3V; 4V; Size≥1812
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## 7. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

No.	Item	Test Condition	Requirements								
13.	Adhesive Strength of Termination	<p>* Capacitors mounted on a substrate. A force of 2N(0201) or 5N(0402~0603) or 10N(&gt;0603) applied perpendicular to the place of substrate and parallel the line joining the center of terminations for 10±1 second.</p>  <p>Capacitor, P.C. Board, Pressurizing force</p>	<p>* No remarkable damage or removal of the terminations.</p>								
14.	Bending Test	<p>* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1mm per second until the deflection becomes 1mm.</p>  <p>Unit : mm</p>	<p>* No remarkable damage.</p> <table border="1"> <thead> <tr> <th>Dielectric</th> <th>Cap. Change</th> </tr> </thead> <tbody> <tr> <td>C0G</td> <td>Within ±5.0% or ±0.5pF, whichever is larger</td> </tr> <tr> <td>X7R, X5R</td> <td>Within ±12.5%</td> </tr> <tr> <td>Y5V</td> <td>Within ±30%</td> </tr> </tbody> </table> <p>(This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test)</p>	Dielectric	Cap. Change	C0G	Within ±5.0% or ±0.5pF, whichever is larger	X7R, X5R	Within ±12.5%	Y5V	Within ±30%
Dielectric	Cap. Change										
C0G	Within ±5.0% or ±0.5pF, whichever is larger										
X7R, X5R	Within ±12.5%										
Y5V	Within ±30%										
15.	Vibration Resistance	<p>* Vibration frequency : 10~55 Hz/min.            * Total amplitude : 1.5mm.            * Test time : 6 hrs. (Two hrs each in three mutually perpendicular directions)            * Before initial measurement (Class II only) : To apply de-aging at 150°C for 1hr then set for 24±2 hrs at room temp.            * Measurement to be made after keeping at room temp. for 24±2 hrs (Class I) or 48±4 hrs (Class II).</p>	<p>* No remarkable damage.            * Cap. change and D.F. : To meet initial spec.</p>								

## 8. PACKAGE DIMENSION AND QUANTITY

Size	Thickness (mm)	Paper tape		Plastic tape	
		7" reel	13" reel	7" reel	13" reel
0201(0603)	0.30±0.03	15k	70k	-	-
	0.30±0.05	15k	-	-	-
	0.30±0.09	15k	-	-	-
0402(1005)	0.50±0.05	10k	50k	-	-
	0.50 +0.02/-0.05	10k	50k	-	-
	0.50±0.20	10k	-	-	-
0603(1608)	0.50±0.10	4k	-	-	-
	0.80±0.07	4k	15k	-	-
	0.80 +0.15/-0.10	4k	15k	-	-
0805(2012)	0.50±0.10	4k	15k	-	-
	0.60±0.10	4k	15k	-	-
	0.80±0.10	4k	15k	-	-
	0.85±0.10	4k	15k	-	-
	1.25±0.10	-	-	3k	10k
1206(3216)	1.25±0.20	-	-	3k	10k
	0.80±0.10	4k	15k	-	-
	0.85±0.10	4k	15k	-	-
	0.95±0.10	-	-	3k	10k
	1.15±0.15	-	-	3k	10k
	1.25±0.10	-	-	3k	10k
	1.60±0.20	-	-	2k	10k
1.60 +0.30/-0.10	-	-	2k	9k	
1210(3225)	0.85±0.10	-	-	3k	10k
	0.95±0.10	-	-	3k	10k
	1.25±0.10	-	-	3k	10k
	1.60±0.20	-	-	2k	-
	2.00±0.20	-	-	1k	6k
1808(4520)	2.50±0.30	-	-	1k	6k
	1.25±0.10	-	-	2k	10k
	1.60±0.20	-	-	2k	8k
1812(4532)	2.00±0.20	-	-	1k	6k
	1.25±0.10	-	-	1k	5k
	1.60±0.20	-	-	1k	-
	2.00±0.20	-	-	1k	-
1825(4563)	2.50±0.30	-	-	0.5k	3k
	2.80±0.30	-	-	0.5k	-
	1.60±0.20	-	-	1k	-
	2.00±0.20	-	-	1k	-
2220(5750)	2.50±0.30	-	-	0.5k	-
	2.80±0.30	-	-	0.5k	-
	1.60±0.20	-	-	1k	-
	2.00±0.20	-	-	1k	-
2225(5763)	2.50±0.30	-	-	0.5k	-
	2.80±0.30	-	-	0.5k	-
	1.60±0.20	-	-	1k	-

Unit : pcs

## 8. PACKAGE DIMENSION AND QUANTITY

### 8.1. EMBOSSED TAPE DIMENSIONS

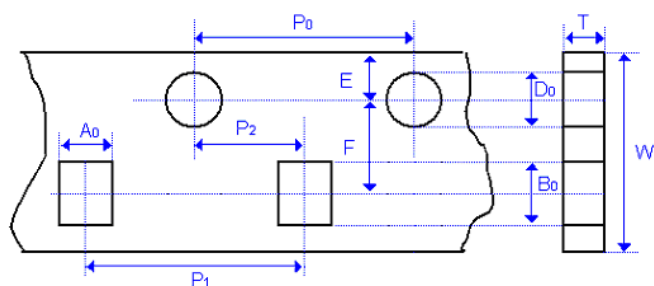


Fig. 8.1 The dimension of paper tape

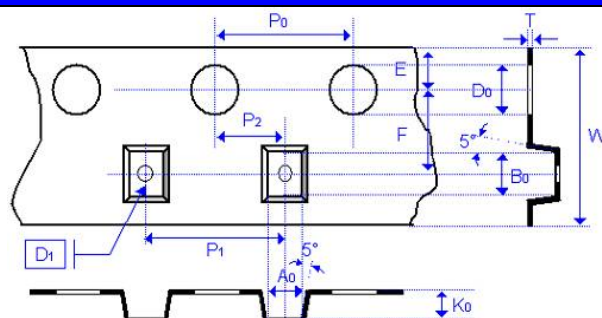


Fig. 8.2 The dimension of plastic tape

Size	0201	0402	0603		0805	
Chip Thickness	0.30±0.03	0.50±0.05 0.50±0.10	0.80±0.07	0.80 +0.15/-0.1	0.80±0.10	1.25±0.10 1.25±0.20
A <sub>0</sub>	0.39±0.07	0.70±0.20	1.00 +0.05/-0.1	1.02 +0.05/-0.1	1.50±0.10	<1.65
B <sub>0</sub>	0.69±0.07	1.20±0.20	1.80±0.10	1.80±0.10	2.30±0.10	<2.40
T	≤0.50	≤0.80	0.95±0.05	0.97±0.05	0.95±0.05	0.23±0.05
K <sub>0</sub>	-	-	-	-	-	<2.50
W	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10
P <sub>0</sub>	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10
10xP <sub>0</sub>	40.00±0.10	40.00±0.10	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20
P <sub>1</sub>	2.00±0.05	2.00±0.05	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10
P <sub>2</sub>	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05
D <sub>0</sub>	1.55±0.05	1.55±0.05	1.55±0.05	1.55±0.05	1.55±0.05	1.50 +0.10/-0
D <sub>1</sub>	-	-	-	-	-	1.00±0.10
E	1.75±0.05	1.75±0.05	1.75±0.05	1.75±0.05	1.75±0.05	1.75±0.10
F	3.50±0.05	3.50±0.05	3.50±0.05	3.50±0.05	3.50±0.05	3.50±0.05
Unit :	mm	mm	mm	mm	mm	mm

Size	1206			1210		1808	
Chip Thickness	0.80±0.10	0.95±0.10 1.25±0.10	1.60±0.20 1.60+0.3/-0/1	0.95±0.10 1.25±0.10 1.60±0.20	2.50±0.30	1.25±0.10 1.60±0.20 2.00±0.20	2.50±0.30
A <sub>0</sub>	2.00±0.10	<2.00	<2.00	<3.05	<3.10	<3.90	<3.90
B <sub>0</sub>	3.50±0.10	<3.60	<3.70	<3.80	<4.00	<5.30	<5.30
T	0.95±0.05	0.23±0.05	0.23±0.05	0.23±0.05	0.23±0.05	0.25±0.05	0.25±0.05
K <sub>0</sub>	-	<2.50	<2.50	<2.50	<3.50	<2.50	<3.00
W	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10	12.00±0.20	12.00±0.20
P <sub>0</sub>	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10
10xP <sub>0</sub>	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20
P <sub>1</sub>	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	8.00±0.10	8.00±0.10
P <sub>2</sub>	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05
D <sub>0</sub>	1.55±0.05	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0
D <sub>1</sub>	-	1.00±0.10	1.00±0.10	1.00±0.10	1.00±0.10	1.50±0.10	1.50±0.10
E	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10
F	3.50±0.05	3.50±0.05	3.50±0.05	3.50±0.05	3.50±0.05	5.50±0.05	5.50±0.05
Unit :	mm	mm	mm	mm	mm	mm	mm

## 8. PACKAGE DIMENSION AND QUANTITY

Size	1812		1825		2220	
Chip Thickness	1.25±0.10 1.60±0.20 2.00±0.20	2.50±0.30	1.60±0.20 2.00±0.20	2.50±0.30	1.40±0.15 1.60±0.20 2.00±0.20	2.50±0.30
A <sub>0</sub>	<3.90	<3.90	<6.80	<6.80	<5.80	<5.80
B <sub>0</sub>	<5.30	<5.30	<5.30	<5.30	<6.50	<6.50
T	0.25±0.05	0.25±0.05	0.30±0.10	0.30±0.10	0.30±0.10	0.30±0.10
K <sub>0</sub>	<2.50	<3.00	<2.50	<3.10	<2.50	<3.10
W	12.00±0.20	12.00±0.20	12.00±0.20	12.00±0.20	12.00±0.20	12.00±0.20
P <sub>0</sub>	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10
10xP <sub>0</sub>	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20	40.00±0.20
P <sub>1</sub>	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10
P <sub>2</sub>	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05
D <sub>0</sub>	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0	1.50 +0.10/-0
D <sub>1</sub>	1.50±0.10	1.50±0.10	1.50±0.10	1.50±0.10	1.50±0.10	1.50±0.10
E	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10	1.75±0.10
F	5.50±0.05	5.50±0.05	5.50±0.05	5.50±0.05	5.50±0.05	5.50±0.05
Unit :	mm	mm	mm	mm	mm	mm

Size	2225	
Chip Thickness	1.60±0.20 2.00±0.20	2.50±0.30
A <sub>0</sub>	<6.80	<6.80
B <sub>0</sub>	<6.50	<6.50
T	0.30±0.10	0.30±0.10
K <sub>0</sub>	<2.50	<3.10
W	12.00±0.20	12.00±0.20
P <sub>0</sub>	4.00±0.10	4.00±0.10
10xP <sub>0</sub>	40.00±0.20	40.00±0.20
P <sub>1</sub>	8.00±0.10	8.00±0.10
P <sub>2</sub>	2.00±0.05	2.00±0.05
D <sub>0</sub>	1.50 +0.10/-0	1.50 +0.10/-0
D <sub>1</sub>	1.50±0.10	1.50±0.10
E	1.75±0.10	1.75±0.10
F	5.50±0.05	5.50±0.05
Unit :	mm	mm

## 8. PACKAGE DIMENSION AND QUANTITY

### 8.2. REEL DIMENSIONS

Size	0201, 0402, 0603, 0805, 1206, 1210			1808, 1812, 1825, 2220, 2225
Reel size	7"	7"	13"	7"
C	13.0 +0.5/-0.2	13.0 +0.5/-0.2	13.0 +0.5/-0.2	13.0 +0.5/-0.2
W <sub>1</sub>	8.4 +1.5/-0	12.4 +2.0/-0	8.4 +1.5/-0	8.4 +1.5/-0
A	178.0 ±0.10	178.0 ±0.10	330.0 ±1.0	178.0 ±0.10
N	60.0 +1.0/-0	80.0 ±1.0	100 ±1.0	60.0 +1.0/-0

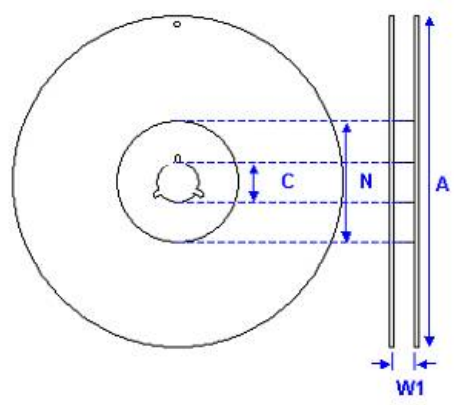


Fig. 8.3 The dimension of reel

## 9. APPLICATION NOTES

### STORAGE

To prevent the damage of solderability of terminations, the following storage conditions are recommended :  
 Indoors under 5 ~ 40°C and 20% ~ 70% RH.

No harmful gases containing sulfuric acid, ammonia, hydrogen sulfide or chlorine.

Packaging should not be opened until the capacitors are required for use. If opened, the pack should be re-sealed as soon as is practicable. Taped product should be stored out of direct sunlight, which might promote deterioration in tape or adhesion performance. The product is recommended to be used within 12 months after shipment and checked the solderability before use.

### HANDLING

Chip capacitors are dense, hard, brittle, and abrasive materials. They are liable to suffer mechanical damage, in the form of cracks or chips. Chip Capacitors should be handled with care to avoid contamination or damage. To use vacuum or plastic tweezers to pick up or plastic tweezers is recommended for manual placement. Tape and reeled packages are suitable for automatic pick and placement machine.

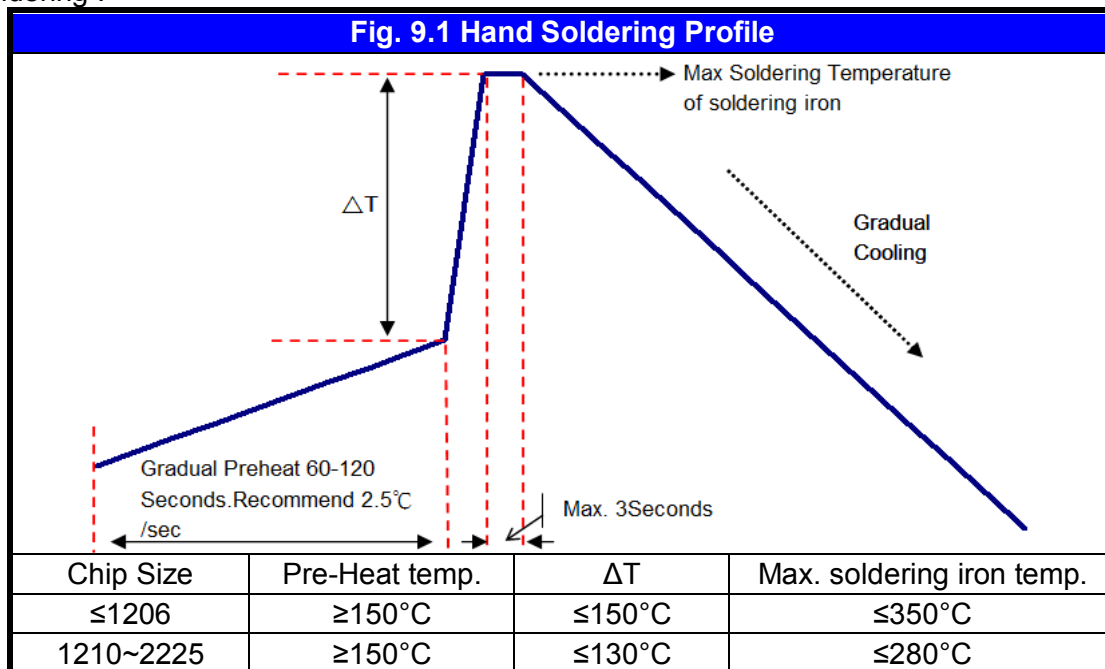
### PREHEAT

In order to minimize the risk of thermal shock during soldering, a carefully controlled preheat is required. The rate of preheat should not exceed 3°C per second.

### SOLDERING

Use mildy activated rosin RA and RMA fluxes do not use activated flux. The amount of solder in each solder joint should be controlled to prevent the damage of chip capacitors caused by the stress between solder, chips, and substrate.

a.) Hand soldering :



\* Soldering iron tip diameter ≤1.0 mm and wattage max. 20W.

\* The Capacitors shall be pre-heated and that the temperature gradient between the devices and the tip of the soldering iron.

\* The required amount of solder shall be melted on the soldering tip.

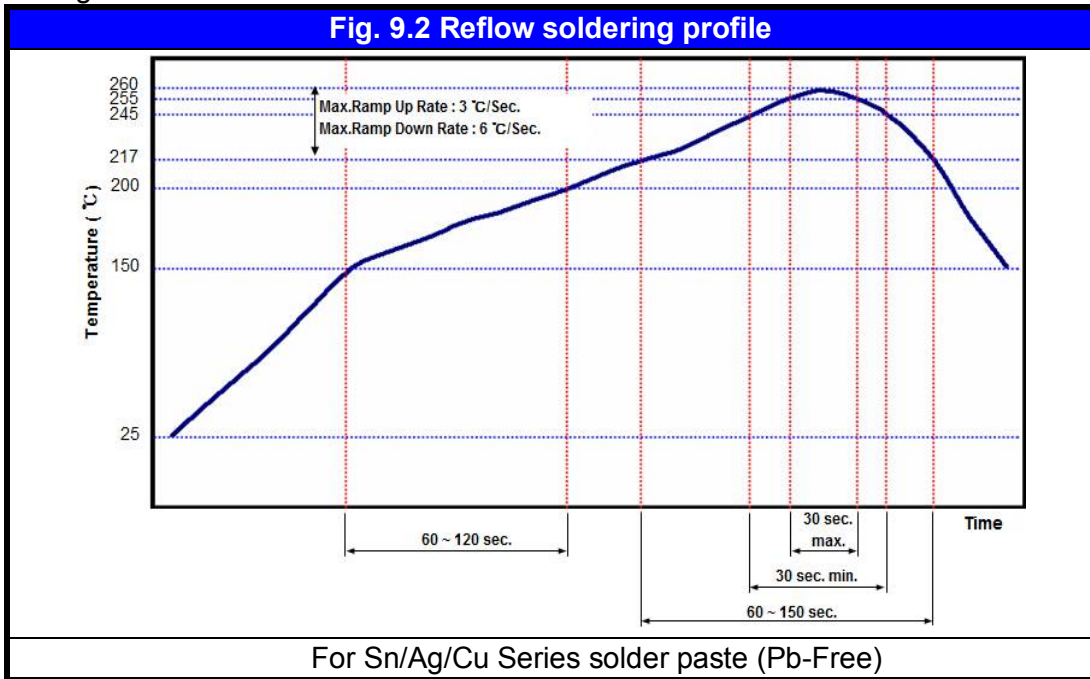
\* The tip of iron should not contact the ceramic body directly.

\* The Capacitors shall be cooled gradually at room temperature after soldering.

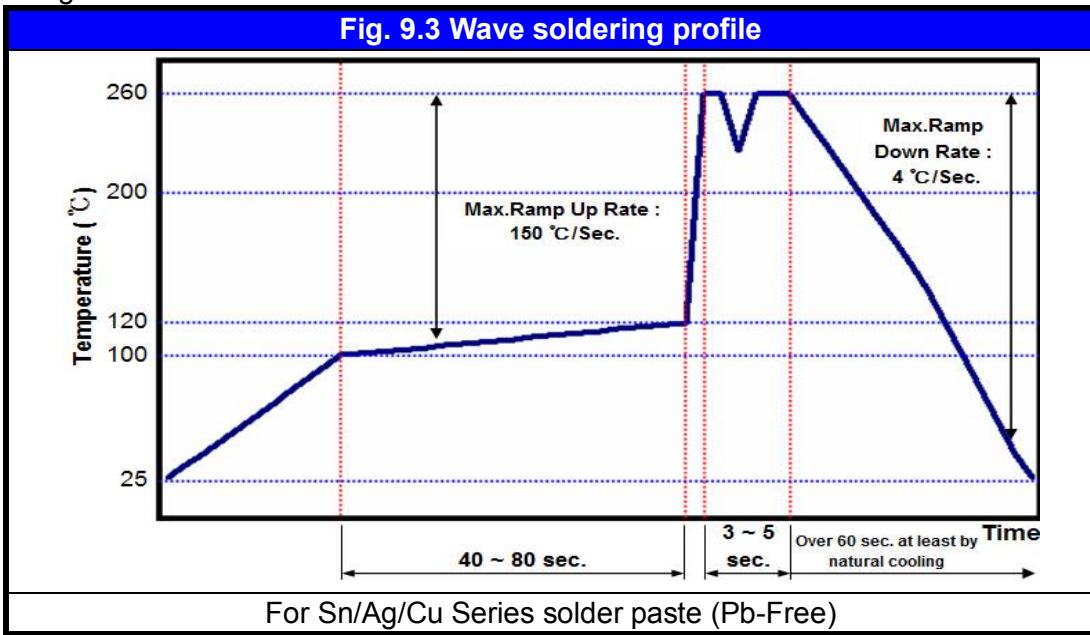
\* Forced air cooling is not allowed.

**9. APPLICATION NOTES**

b.) Reflow soldering :



c.) Wave soldering :



Soldering conditions :

Class I :

Size Inch (mm)	Temper. Cher.	Capacitance	Condition	
			Wave	Reflow
≤0402 (1005)	All Class I	All	X	O
0603 (1608)	All Class I	All	O	O
0805 (2012)	All Class I	All	O	O
1206 (3216)	All Class I	All	O	O
≥1210 (3225)	All Class I	All	X	O

## 9. APPLICATION NOTES

Soldering conditions :

Class II :

Size Inch (mm)	Temper. Cher.	Capacitance	Condition	
			Wave	Reflow
≤0402 (1005)	All Class II	All	X	O
0603 (1608)	All Class II	Cap. <2.2μF	O	O
		Cap. ≥2.2μF	X	O
0805 (2012)	All Class II	Cap. <4.7μF	O	O
		Cap. ≥4.7μF	X	O
1206 (3216)	All Class II	Cap. <4.7μF	O	O
		Cap. ≥4.7μF	X	O
≥1210 (3225)	All Class II	All	X	O

Soldering height :

<p>The solder climbing minimum height is suggesting to 25% of chip thickness or 500um whichever is less. (Reference from IPC-610E)</p>	
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### COOLING

After soldering, cool the chips and the substrate gradually to room temperature. Natural cooling in air is recommended to minimize stress in the solder joint.

### CLEANING

All flux residues must be removed by using suitable electronic-grade vapor-cleaning solvents to eliminate contamination that could cause electrolytic surface corrosion. Good results can be obtained by using ultrasonic cleaning of the solvent. The choice of the proper system is depends upon many factors such as component mix, flux, and solder paste and assembly method. The ability of the cleaning system to remove flux residues and contamination from under the chips is very important.